

S/N 10/020,854

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Steven L. Scott et al.	Examiner:	Venkatesh Haliyur
Serial No.:	10/020,854	Group Art Unit:	2619
Filed:	December 14, 2001	Docket:	499.710US1
Title:	NODE TRANSLATION AND PROTECTION IN A CLUSTERED MULTIPROCESSOR SYSTEM		

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EXAMINER INTERVIEW SUMMARY

MS ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicant thanks Examiner Haliyur for the telephonic interview on November 7, 2007 with Applicant's representative Thomas Brennan. Claims 1-27 were discussed and it was agreed that a Terminal Disclaimer would be filed, which was done on November 12, 2007.

The Examiner is invited to telephone Applicant's attorney at (612) 373-6909 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6909

Date

December 13, 2007

By

Thomas F. Brennan

Thomas F Brennan

Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 13 day of December 2007.

Name

Lisa Posorske

Signature

Lisa Posorske